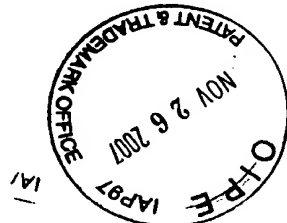


FIG. 1



High-order Low-order	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0 0000	BRSET0	BSET0	BRA	NEG	NEG	NEG	0110	NEG	RTI	1001	SUB	SUB	SUB	SUB	SUB	SUB
1 0001	BRSET1	BSET1	BRN						RTS		CMP	CMP	CMP	CMP	CMP	CMP
2 0010	BRSET2	BSET2	BHI								SBC	SBC	SBC	SBC	SBC	SBC
3 0011	BRSET3	BSET3	BLS	COM	COM	COM	COM	COM	SWI		CPX	CPX	CPX	CPX	CPX	CPX
4 0101	BRSET4	BSET4	BCC	LSR	LSR	LSR	LSR	LSR			AND	AND	AND	AND	AND	AND
5 0101	BRSET5	BSET5	BCS								BIT	BIT	BIT	BIT	BIT	BIT
6 0110	BRSET6	BSET6	BNE	ROR	ROR	ROR	ROR	ROR			LDA	LDA	LDA	LDA	LDA	LDA
7 0111	BRSET7	BSET7	BEQ	ASR	ASR	ASR	ASR	ASR		TAX		STA	STA	STA	STA	STA
8 1000	BRCLR0	BCLR0	BHCC	LSL	LSL	LSL	LSL	LSL		CLC	EOR	EOR	EOR	EOR	EOR	EOR
9 1001	BRCLR1	BCLR1	BHCS	ROL	ROL	ROL	ROL	ROL		SEC	ADC	ADC	ADC	ADC	ADC	ADC
A 1010	BRCLR2	BCLR2	BPL	DEC	DEC	DEC	DEC	DEC		CLI	ORA	ORA	ORA	ORA	ORA	ORA
B 1011	BRCLR3	BCLR3	BMI							SEI	ADD	ADD	ADD	ADD	ADD	ADD
C 1100	BRCLR4	BCLR4	BMC	INC	INC	INC	INC	INC		RSP		JMP	JMP	JMP	JMP	JMP
D 1101	BRCLR5	BCLR5	BMS	TST	TST	TST	TST	TST		NOP	BSR	JSR	JSR	JSR	JSR	JSR
E 1110	BRCLR6	BCLR6	BIL								LDX	LDX	LDX	LDX	LDX	LDX
F 1111	BRCLR7	BCLR7	BIH	CLR	CLR	CLR	CLR	CLR	WAIT	TXA		STX	STX	STX	STX	STX

FIG. 2

High-order Low-order	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0 0000	FRD	RTI	BSET0	BSET0	BRA	NEG	NEG	NEG	NEG	1001	1010	1011	1100	1101	1110	1111
1 0001	FRD	RTS	BSET1	BSET1	BRN	FRD	FRD	FRD	FRD	FRD	CMP	CMP	CMP	CMP	SUB	SUB
2 0010	FRD	FRD	BSET2	BSET2	BHI	FRD	FRD	FRD	FRD	FRD	SBC	SBC	SBC	SBC	SBC	SBC
3 0011	FRD	SWI	BSET3	BSET3	BLS	COM	COM	COM	COM	COM	CPX	CPX	CPX	CPX	CPX	CPX
4 0101	FRD	FRD	BSET4	BSET4	BCC	LSR	LSR	LSR	LSR	LSR	AND	AND	AND	AND	AND	AND
5 0101	FRD	FRD	BSET5	BSET5	BCS	FRD	FRD	FRD	FRD	FRD	BIT	BIT	BIT	BIT	BIT	BIT
6 0110	FRD	FRD	BSET6	BSET6	BNE	ROR	ROR	ROR	ROR	ROR	LDA	LDA	LDA	LDA	LDA	LDA
7 0111	TAX	FRD	BSET7	BSET7	BEQ	ASR	ASR	ASR	ASR	ASR	STA	STA	STA	STA	STA	FRD
8 1000	CLC	FRD	BCLR0	BCLR0	BHCC	LSL	LSL	LSL	LSL	LSL	EOR	EOR	EOR	EOR	EOR	EOR
9 1001	SEC	FRD	BCLR1	BCLR1	BHCS	ROL	ROL	ROL	ROL	ROL	ADC	ADC	ADC	ADC	ADC	ADC
A 1010	CLI	FRD	BCLR2	BCLR2	BPL	DEC	DEC	DEC	DEC	DEC	ORA	ORA	ORA	ORA	ORA	ORA
B 1011	SEI	FRD	BCLR3	BCLR3	BMI	FRD	FRD	FRD	FRD	FRD	ADD	ADD	ADD	ADD	ADD	ADD
C 1100	RSP	FRD	BCLR4	BCLR4	BMC	INC	INC	INC	INC	INC	JMP	JMP	JMP	JMP	JMP	FRD
D 1101	NOP	FRD	BCLR5	BCLR5	BMS	TST	TST	TST	TST	TST	JSR	JSR	JSR	JSR	JSR	BSR
E 1110	FRD	FRD	BCLR6	BCLR6	BIL	FRD	FRD	FRD	FRD	FRD	LDX	LDX	LDX	LDX	LDX	LDX
F 1111	TXA	WAIT	BCLR7	BCLR7	BIH	CLR	CLR	CLR	CLR	CLR	STX	STX	STX	STX	STX	FRD

FIG. 3

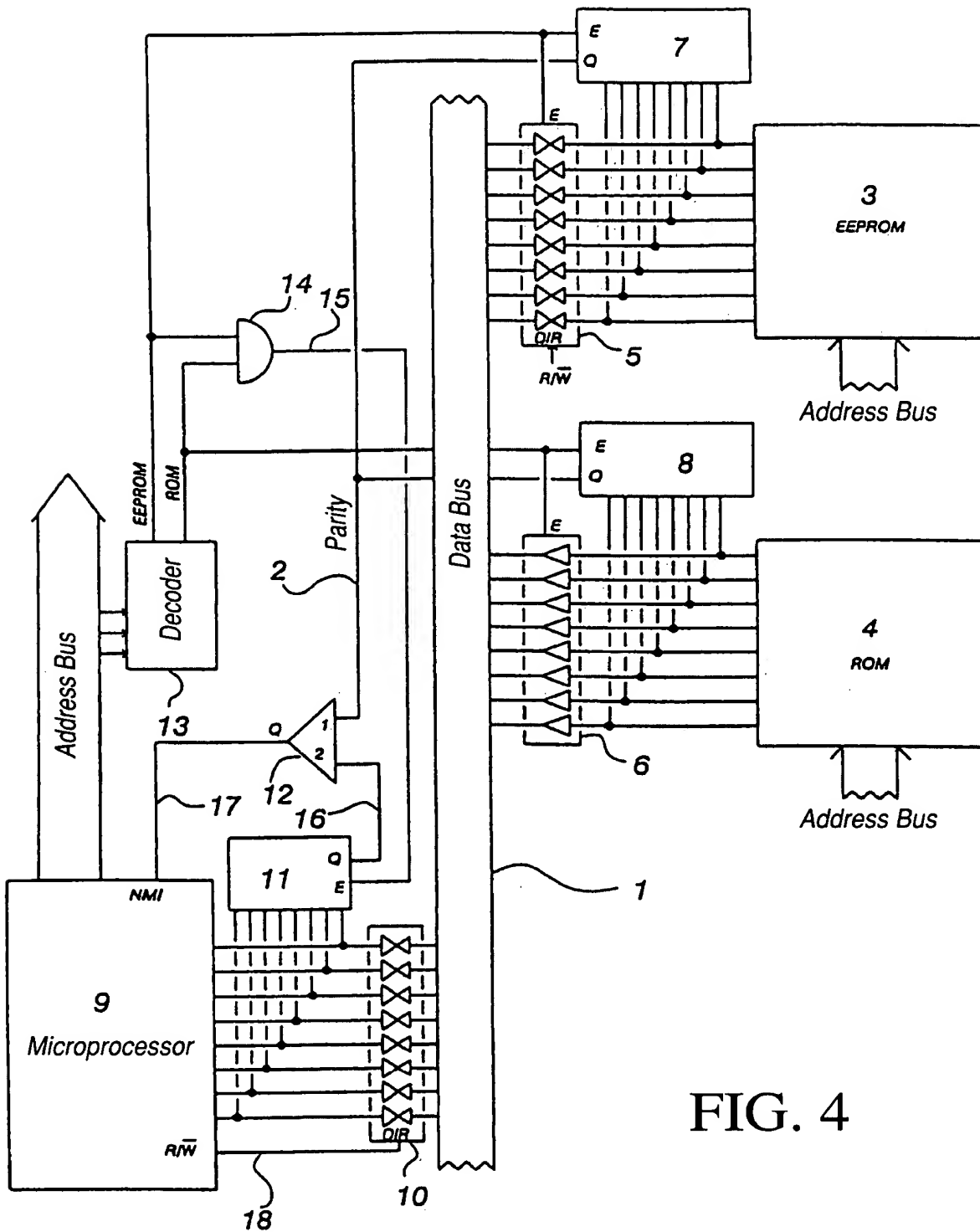


FIG. 4

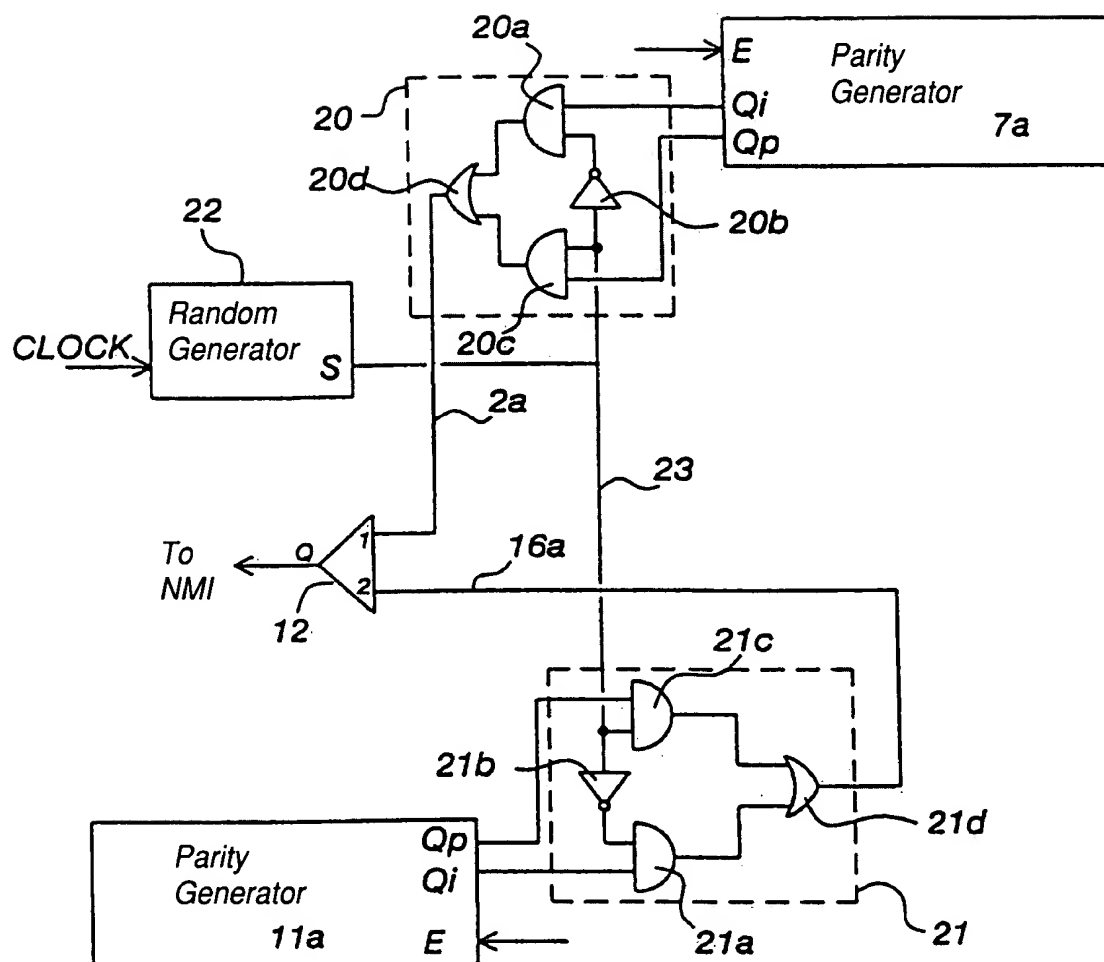


FIG. 5

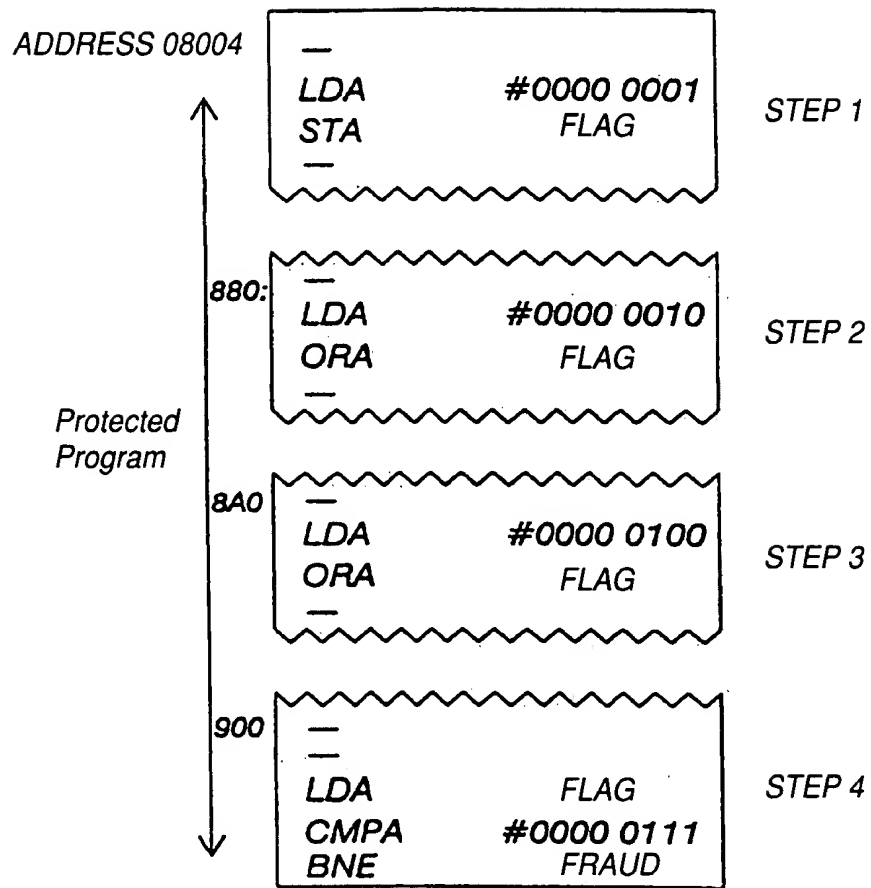


FIG. 6

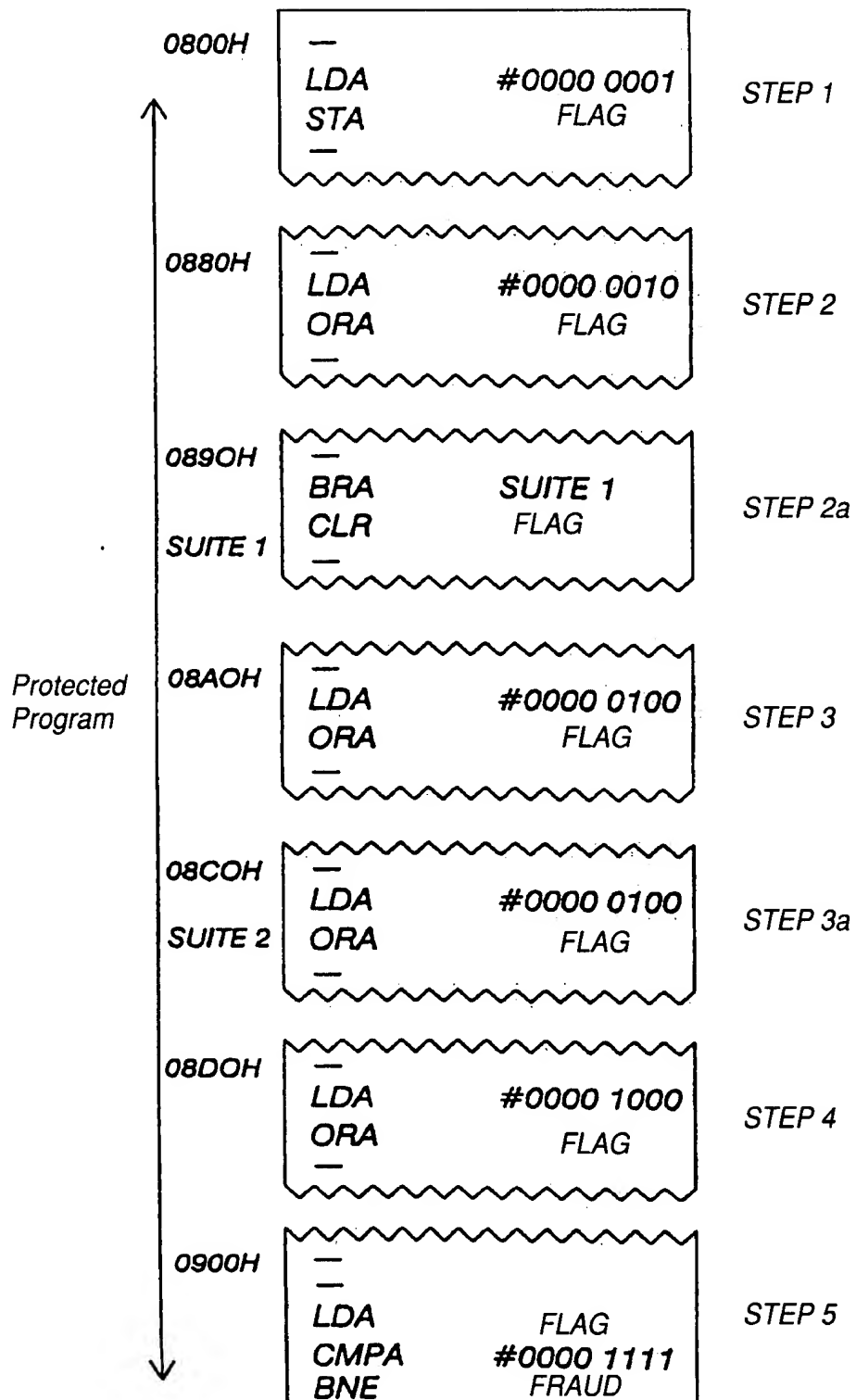


FIG. 7